

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

J. KONNO et al

Serial Number: 07/743,383

Group Art Unit: 1104

Filed: August 21, 1991

Examiner: G. Goudreau

For: METHOD FOR PRODUCING SEMICONDUCTOR INTEGRATED CIRCUITS
AND APPARATUS USED IN SUCH METHOD

PETITION FOR EXTENSION OF TIME

Honorable Commissioner
of Patents and Trademarks
Washington, D.C. 20006

December 27, 1993

Sir:

Applicants petition the Commissioner of Patents and Trademarks to extend the time for response to the Office Action dated August 25, 1993, for one month from November 25, 1993, to December 25, 1993.

Enclosed is a check in the amount of One Hundred Ten Dollars (\$110.00) to cover the cost of the extension fee. In the event that this check is found to be insufficient, or if any additional fees are due with respect to the filing of this paper, please charge Deposit Account Number 14-1060.

Respectfully submitted,

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